

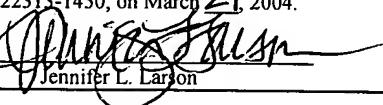


PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: LETAVIC et al. Examiner: Ngo, N.
Serial No.: 10/015,640 Group Art Unit: 2814
Filed: December 10, 2001 Docket No.: US010631
Title: HIGH FREQUENCY HIGH VOLTAGE SILICON-ON-INSULATOR
DEVICE WITH MASK VARIABLE INVERSION CHANNEL AND
METHOD FOR FORMING THE SAME

CERTIFICATE UNDER 37 CFR 1.8: The undersigned hereby certifies that this correspondence and the papers, as described hereinabove, are being deposited in the United States Postal Service, as first class mail, in an envelope addressed to: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450, on March 21, 2004.

By: 
Jennifer L. Larson

PETITION FOR EXTENSION OF TIME

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

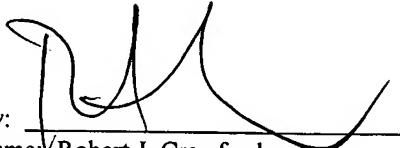
Sir:

In accordance with the provisions of 37 C.F.R. §1.136, it is respectfully requested that a three-month extension of time be granted in which to file the attached REQUEST FOR CONTINUED EXAMINATION. Please charge Deposit Account No. 50-0996 (VLSI.406PA) in the amount of **\$950.00** to cover the extension fee for a large entity. If necessitated by this Extension, authority is given to charge/credit additional fees/overages to the above-referenced Deposit Account.

Respectfully Submitted,

Please direct all correspondence to:

Corporate Patent Counsel
Philips Intellectual Property & Standards
1000 West Maude Ave., W2-755
Sunnyvale, CA 94085-2810

By: 
Name: Robert J. Crawford
Reg. No.: 32,122
(VLSI.406PA)